EQP314X-10D-M

QSFP+ 40Gb/s PSM4 MPO 10km DDM Optical Transceiver

PRODUCT FEATURES

- Four-channel full-duplex transceiver modules
- Transmission data rate up to 11.2Gbit/s per channel
- Up to 10km transmission of single mode fiber
- Low power consumption <2.5W
- Operating case temperature 0℃ to 70℃
- 3.3V power supply voltage
- RoHS compliant
- Hot Pluggable QSFP+ form factor
- Single MPO connector receptacle
- Built-in digital diagnostic function

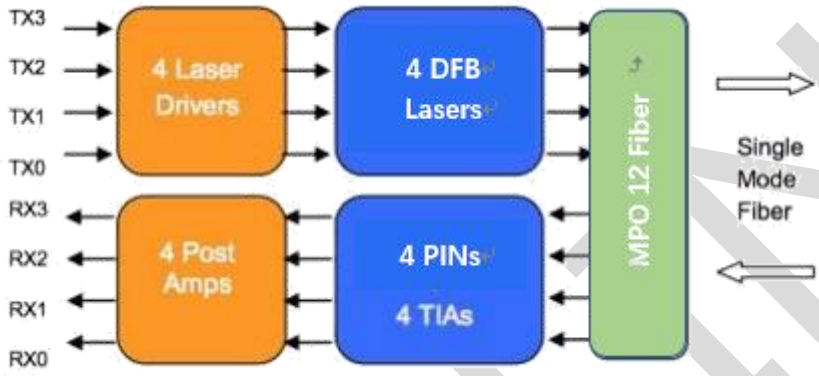
APPLICATIONS

- Infiniband QDR and DDR interconnects
- 40G Ethernet
- Proprietary High Speed Interconnections
- Data center

DESCRIPTIONS

The ETU-LINK EQP314X-10D-M is a Four-Channel, Pluggable, Parallel, Fiber-Optic QSFP+ Transceiver for InfiniBand QDR/DDR/SDR, 10G/8G/4G/2G fiber channel, PCIe and SAS Applications. The QSFP full-duplex optical module offers 4 independent transmit and receive channels, each capable of 10.3Gbps operation for an aggregate data rate of 40Gbps 2km using single mode fiber. These modules are designed to operate over single mode fiber systems using 1310nm DFB laser array. An optical fiber ribbon cable with an MPO/MTPTM connector can be plugged into the QSFP module receptacle. QSFP+ PSM LR4 is one kind of parallel transceiver which provides increased port density and total system cost savings.

Module Block Diagram



Ordering Information

Part No.	Data Rate(optical)	Laser	Fiber Type	Distance	Optical Interface	Temp	DDMI	Latch Color
EQP314X-10D-M	41.25Gb/s	DFB	SMF	10km	MPO	0~70	Yes	Blue

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	TS	-40	+85	°C
Maximum Supply Voltage	VCC	-0.5	3.6	V
Operating Relative Humidity	RH	0	85	%

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Operating Case Temperature	Top	0		+70	°C	
Power Supply Voltage	V_{CC}	3.13	3.3	3.47	V	
Power Consumption			1.7	2.5	W	Power Consumption

Aggregate Bit Rate	BR_{AVE}		41.25		Gb/s	
Lane Bit Rate	BR_{LANE}		10.3125		Gb/s	
Transmission Distance	TD			10	km	
Coupled fiber	Single mode fiber					9/125um SMF

Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Differential input impedance	Zin	90	100	110	ohm	
Differential Output impedance	Zout	90	100	110	ohm	
Differential input voltage amplitude	ΔVin	300		1100	mVp-p	
Differential output voltage amplitude	ΔVout	500		800	mVp-p	
Bit Error Rate	BR			E-12		
Input Logic Level High	VIH	2.0		VCC	V	
Input Logic Level Low	VIL	0		0.8	V	
Output Logic Level High	VOH	VCC-0.5		VCC	V	
Output Logic Level Low	VOL	0		0.4	V	

Optical and Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Center Wavelength	λC	1270	1310	1350	nm	1
RMS Spectral Width	λrms			3.5	nm	1
Average Launch Power, each lane	PAVG	-5.5	-0.5	+1.5	dBm	
Optical Modulation Amplitude (OMA)	POMA	-4.5	-0.5	+2.0	dBm	1
Difference in Launch Power between any two lanes	Ptx,diff			5.0	dB	
Launch Power in OMA minus Transmitter and Dispersion Penalty (TDP), each Lane	OMA-TDP	-9.7			dBm	1
Rise/Fall Time	Tr/Tf			50	ps	
Extinction Ratio	ER	3.5			dB	
Relative Intensity Noise	Rin			-128	dB/Hz	
Optical Return Loss Tolerance	TOL			12	dB	
Transmitter Reflectance	RT			-12	dB	
Transmitter Eye Mask Margin	EMM	10			%	2
Transmitter Eye Mask Definition {X1, X2, X3, Y1, Y2, Y3}		{0.25, 0.4, 0.45, 0.25, 0.28, 0.4}				
Average Launch Power OFF Transmitter, each Lane	Poff			-30	dBm	

Receiver						
Center Wavelength	λC	1270	1310	1350	nm	
Damage Threshold	THd	+3			dBm	
Overload, each lane	OVL	+1			dBm	
Receiver Sensitivity in OMA, each Lane	SEN			-12.5	dBm	
Difference in Receive Power between any two Lanes (OMA)	Prx,diff			5.0	dB	
Signal Loss Assert Threshold	LOSA	-30			dBm	
Signal Loss Deassert Threshold	LOSD			-15	dBm	
LOS Hysteresis	LOSH	0.5		6	dB	
Optical Return Loss	ORL			-12	dBm	
Receive Electrical 3 dB upper Cutoff Frequency, each Lane	Fc			12	GHz	

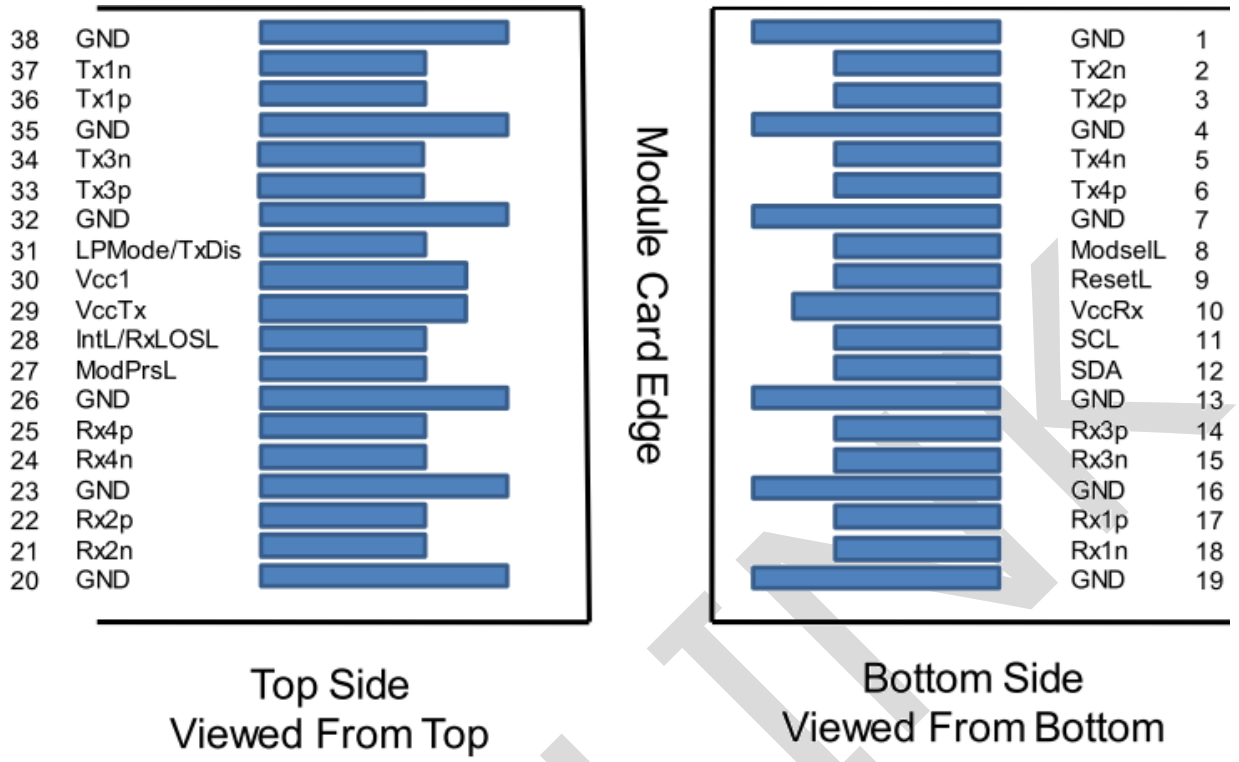
Notes:

1. Transmitter wavelength, RMS spectral width and power need to meet the OMA minus TDP specs to guarantee link performance.
2. The eye diagram is tested with 1000 waveform.

Digital Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal / External
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	10 to 100	mA	±10%	Internal / External
TX Power	--5.5 to 1.5	dBm	±3dB	Internal / External
RX Power	-12.5 to 1.5	dBm	±3dB	Internal / External

Pin Diagram



Pin Definitions

PIN	Logic	Symbol	Description	Plug Seq.	Notes
1		GND	Ground	1	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	3	
4		GND	Ground	1	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	3	
7		GND	Ground	1	1
8	LVTLL-I	ModSelL	Module Select	3	
9	LVTLL-I	ResetL	Module Reset	3	
10		VccRx	+ 3.3V Power Supply Receiver	2	2
11	LVCMOS-I/O	SCL	2-Wire Serial Interface Clock	3	
12	LVCMOS-I/O	SDA	2-Wire Serial Interface Data	3	
13		GND	Ground	1	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3	
15	CML-O	Rx3n	Receiver Inverted Data Output	3	
16		GND	Ground	1	1

17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3	
18	CML-O	Rx1n	Receiver Inverted Data Output	3	
19		GND	Ground	1	1
20		GND	Ground	1	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3	
23		GND	Ground	1	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3	
26		GND	Ground	1	1
27	LVTTL-O	ModPrsL	Module Present	3	
28	LVTTL-O	IntL/Rx_LOS	Interrupt/Rx_LOS	3	
29		VccTx	+3.3 V Power Supply transmitter	2	2
30		Vcc1	+3.3 V Power Supply	2	2
31	LVTTL-I	LPMode/TxDIS	Low Power Mode/Tx_Disable	3	
32		GND	Ground	1	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	3	
34	CML-I	Tx3n	Transmitter Inverted Data Output	3	
35		GND	Ground	1	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3	
37	CML-I	Tx1n	Transmitter Inverted Data Output	3	
38		GND	Ground	1	1

Notes:

1. GND is the symbol for signal and supply (power) common for the QSFP28 module. All are common within the QSFP28 module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.
2. Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power supplies and shall be applied concurrently. Requirements defined for the host side of the Host Edge Card Connector are listed in MSA. The connector pins are each rated for a maximum current of 1000 mA.

Recommended Interface Circuit

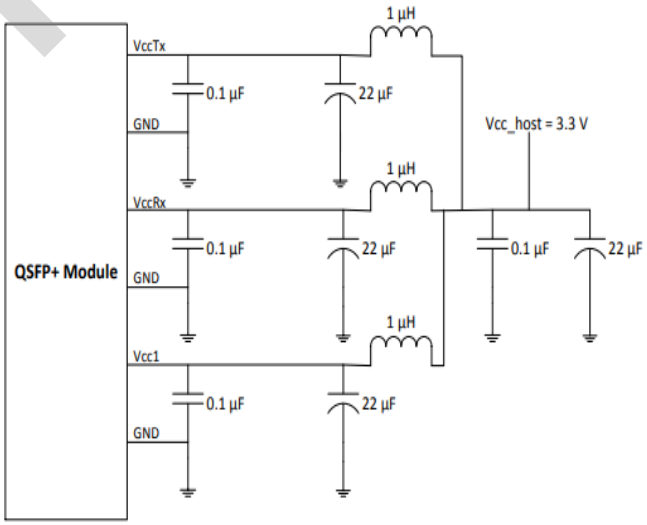


FIGURE 5-4 RECOMMENDED HOST_BOARD_POWER SUPPLY FILTERING

Optical Interface Lanes and Assignment

The optical interface port is a male MPO connector. The four fiber positions on the left as shown in Figure 2, with the key up, are used for the optical transmit signals (Channel 1 through4). The fiber positions on the right are used for the optical receive signals (Channel 4 through 1). The central four fibers are physically present.

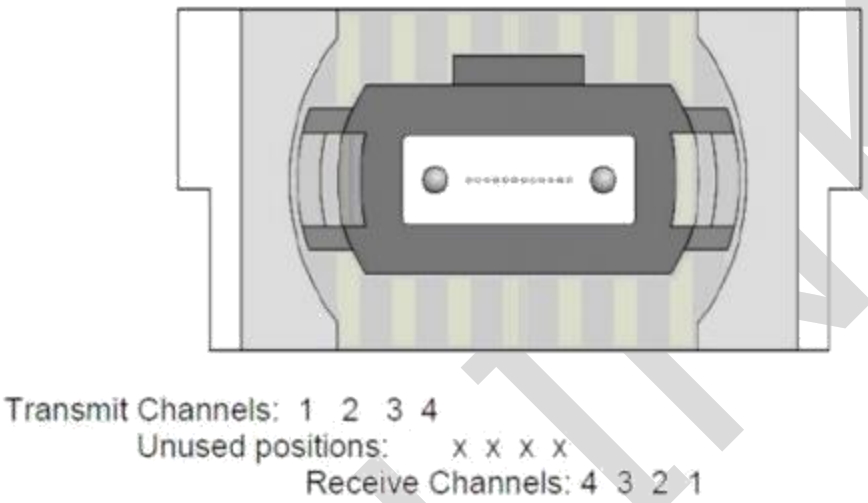
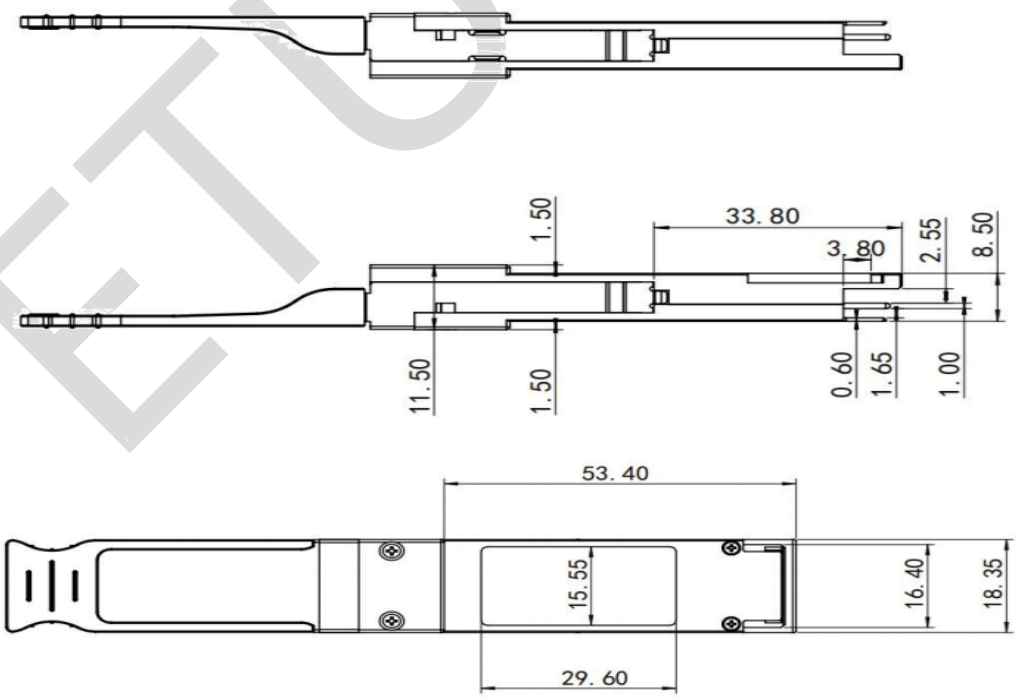
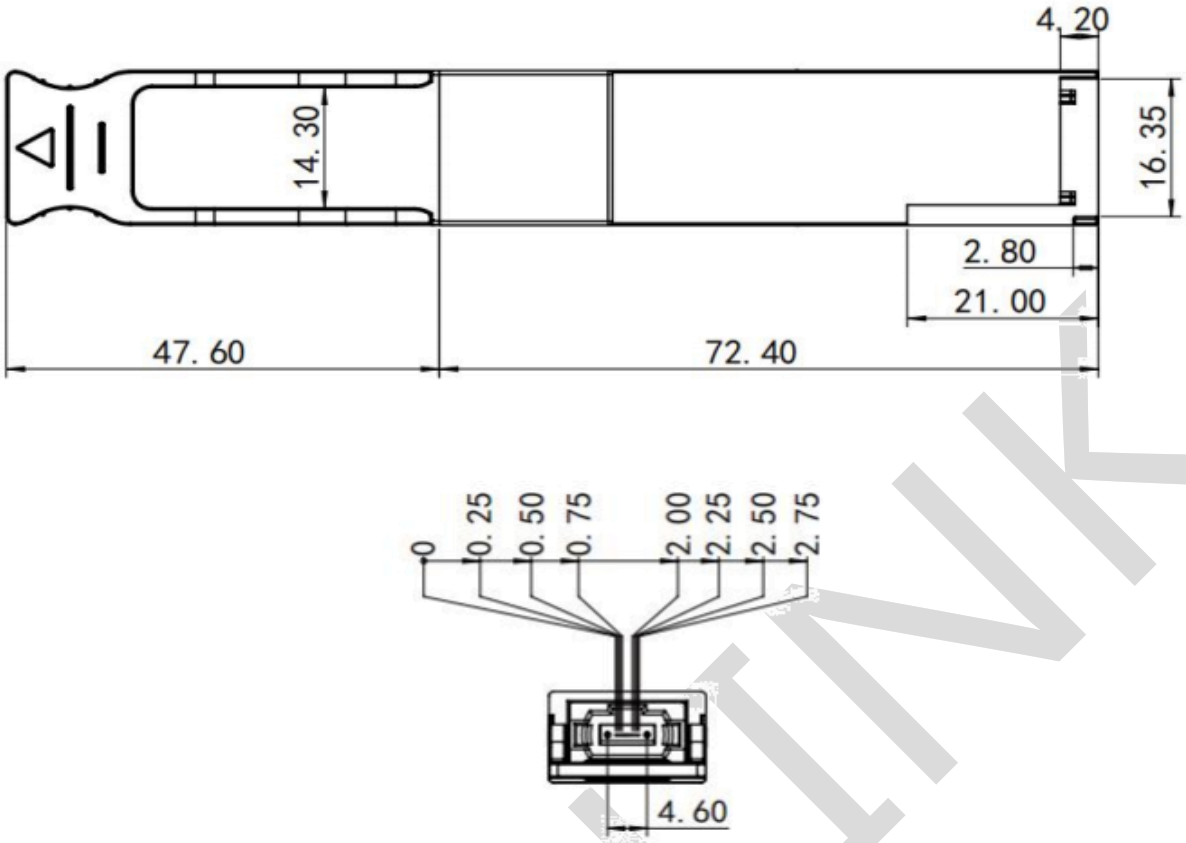


Figure 2. Optical Receptacle and Channel Orientation

Mechanical Diagram





Revision History

Version No.	Date	Description
1.0	July 15, 2017	Preliminary datasheet
1.1	July 20,2024	Format change

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