

Rev	Date	Modified by	Description
A0	2023		

Product Specifications

40G QSFP+ SR4 850nm 150m Optical Transceiver

PN: EQP854X-01D

Features

QSFP+ Serial Optical Interface

- High quality and reliability optical device and Sub-assemblies
- 4 VCSEL Lasers and 4 channels PIN photo detectors
- Maximum link length of 100m on OM3 MMF or 150m on OM4 MMF

QSFP+ MSA Compliant

- Compliant with SFF-8436 for electrical interface
- Compliant with SFF-8436 for mechanical interface
- QSFP+ Mechanical Interface for easy removal
- MPO Receptacle
- Compliant with SFF-8436 for 2-wire interface for management and DDM

Support Protocol

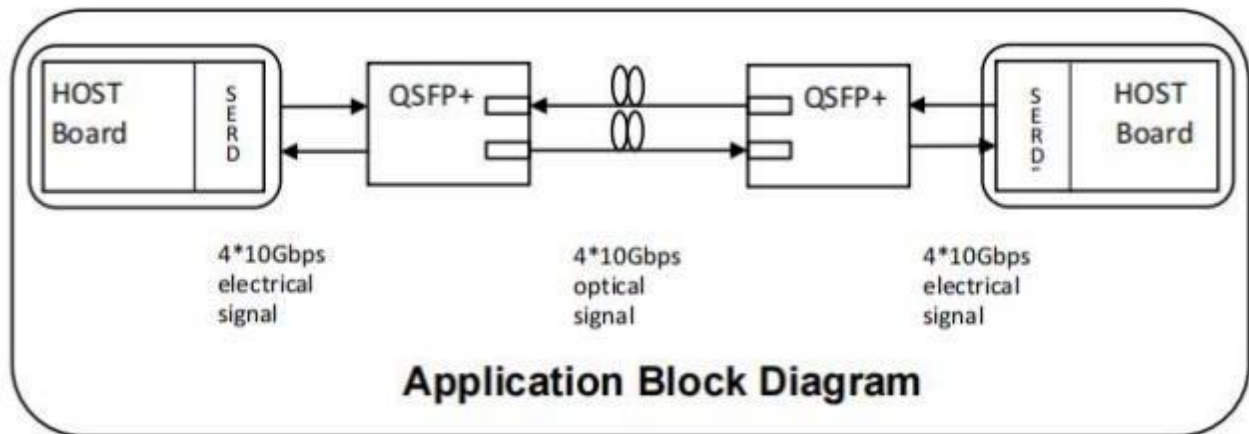
- IEEE Std802.3ba
- SFF-8436

Low Power Consumption

- Less than 1.5W in temperature range of 0 to 70°C

Applications

- 40G SR4 Ethernet links
- Data center
- Other high speed data connections



General Description

It is a Four-Channel, Pluggable, Fiber-Optic QSFP+ SR4 for 40Gigabit Ethernet and Infiniband EDR applications. This transceiver is a high performance module for data communication and interconnect applications. It integrates four data lanes in each direction with 40Gbps bandwidth. The length of QSFP+ SR4 is up to 100 meters over OM3 MMF or 150 meters over OM4 MMF. This module is designed to operate over multimode fiber systems using a nominal wavelength of 850nm.

Functional Description

It contains a MPO connector for the optical interface and a 38-pin connector for the electrical interface.

Transmitter Operation

The module converts 4 input channels of 10Gb/s electrical data to 4 channels optical signals and then transmit them with MPO connector for 40Gb/s optical transmission.

Receiver Operation

The module receive a 40Gb/s optical input into 4 channels of 850nm optical signals and then converts them to 4 output channels of 10Gb/s electrical data.

Management Interface

A 2-wire interface (SCL, SDA) is used for serial ID, digital diagnostics and other control and monitor functions.

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Storage Temperature	Ts	-40	85	°C
Case Operating Temperature	Top	0	70	°C
Relative Humidity (non-condensation)	RH	5	95	%
Supply Voltage	Vcc	-0.5	3.6	V
Input Voltage	Vin	-0.5	VCC+0.5	V

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Operating Case Temperature	Top	0	70	°C
Relative Humidity(non- condensation)	RH	5	85	%
Power Supply Voltage	Vcc	3.135	3.465	V
Total Power Consumption	Pc	-	1.5	W

Transmitter Optical Interface

Parameter	Symbol	Min	Typical	Max	Unit
Center Wavelength	λ_c	840	850	860	nm
Data rate each lane	DR		10.3125		Gbps
Average Launch Power	Pavg	-7.6		2.4	dBm
Transmit OMA each lane	TxOMA	-5.6		3	dBm
Peak Power each lane	PPx			4	dBm
Extinction ratio	ER	3			dB
TDP each lane	TDP			3.5	dB

Spectral Width , RMS				0.65	nm
Optical Return Loss Tolerance	ORLT			12	dB
Optical Power for TX DISABLE				-30	dBm

Receiver Optical Interface

Parameter	Symbol	Min	Typical	Max	Unit
Center Wavelength		840	850	860	nm
Data rate each lane	DR		10.3125		Gbps
Average Receive Power each lane	RXPx	-9.5		2.4	dBm
Receive Power in OMA(Each Lane),Overload	RXOMA	2.4			dBm
Stressed Receiver Sensitivity (OMA) each Lane	SRS			-5.4	dBm
Receiver Sensitivity (OMA) each lane				-9.5	dBm
Peak power, each lane	PPx			4	dBm
Receiver Reflectance	Rfl			-12	dB
Rx LOS	Assert			-30	dBm
	De-assert			-10	dBm
	Hysteresis		0.5		dB

Hig hspeed Electrical specifications

Parameters	Min	Typical	Max	Unit
Supply voltage	3.135		3.465	V
Supply current			450	mA
Input differential impedance	90	100	110	Ω

Differential data input swing	300		1100	mVpp
Differential data output swing	300		850	mVpp
Input Logic Level High	2		Vcc	V
Input Logic Level Low	0		0.8	V
Output Logic Level High	Vcc-0.5		Vcc	V
Output Logic Level Low	0		0.4	V

Wire Electricals pecifications

Parameter	Symbol	Min	Max	Unit
Host 2-wire Vcc voltage	Vcc_Host_2w	3.14	3.46	V
SCL and SDA Voltage ^[1]	VOL	0.0	0.40	V
	VOH	Vcc_Host_2w - 0.5	Vcc_Host_2w + 0.3	V
	VIL	-0.3	VccT*0.3	V
	VIH	VccT*0.7	VccT+0.5	V
Input current on the SCL and SDA contacts	I _I	-10	10	mA

Module power supply specification

Parameter	Symbol	Min	Max	Unit
Instantaneous peak current at hot plug	I _{cc_ip}		600	mA
Sustained peak current at hot plug	I _{cc_sp}		495	mA

User Interface

Management Interface

QSFP+ 2-Wire Serial Interface Protocol

QSFP+ 2-wire serial interface is specified in the SFF-8436. The QSFP+ 2-wire serial interface is used for serial ID, digital diagnostics, and certain control functions. The 2-wire serial interface is mandatory for all QSFP+ modules.

QSFP+ Management Interface

The common memory map for managed external cable interfaces is utilized for serial ID, digital monitoring and control functions. The map is arranged into a single lower page address space of 128 bytes and multiple upper address pages.

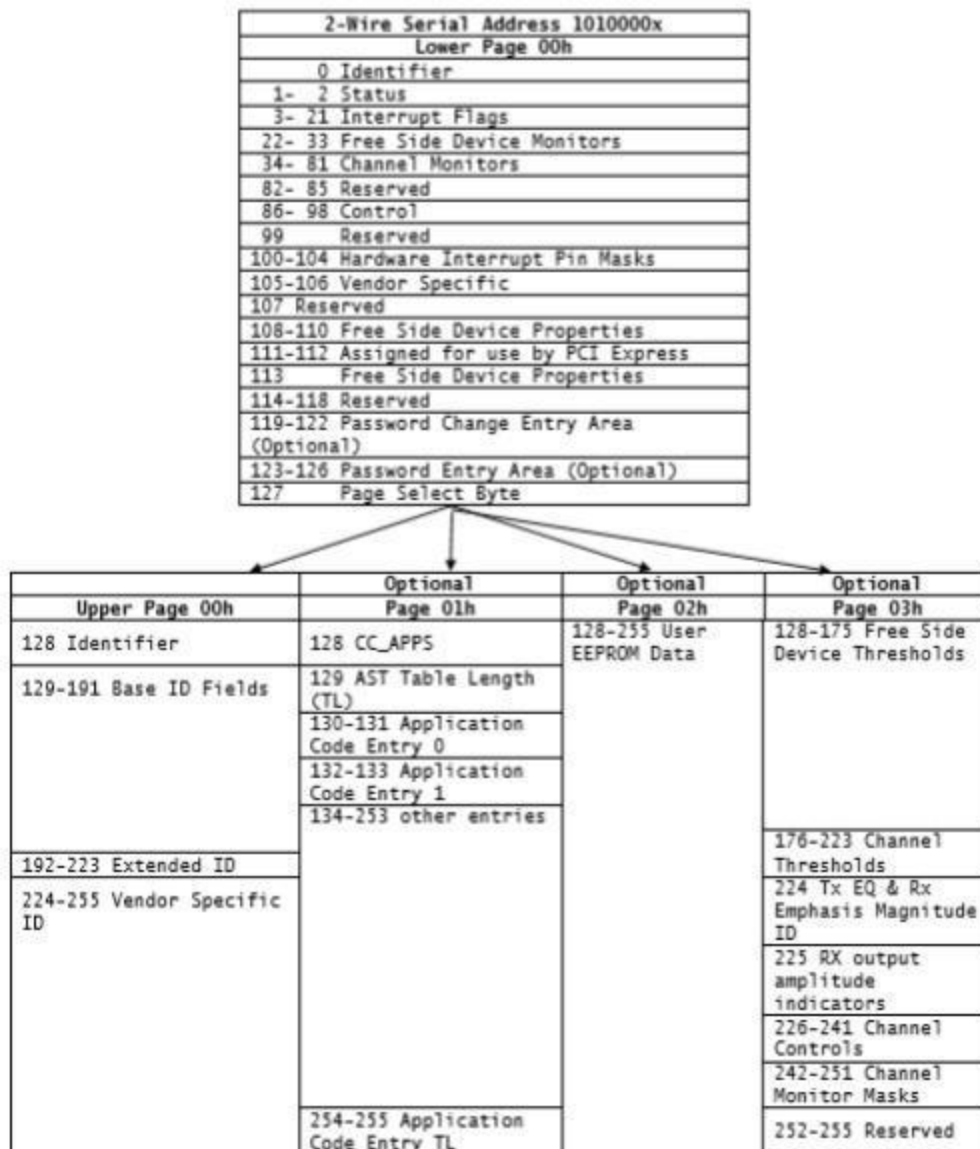


Figure 6.1 QSFP+ Memory Map

Memory Map in detail

Byte Address	Description	Type
0	Identifier (1 Byte)	Read Only
1-2	Status (2 Bytes)	Read Only
3-21	Interrupt Flags (31 Bytes)	Read Only
22-33	Module Monitors (12 Bytes)	Read Only
34-81	Channel Monitors (48 Bytes)	Read Only
82-85	Reserved (4 Bytes)	Read Only
86-97	Control (12 Bytes)	Read/Write
98-99	Reserved (2 Bytes)	Read/Write
100-106	Module and Channel Masks (7 Bytes)	Read/Write
107-118	Reserved (12 Bytes)	Read/Write
119-122	Reserved (4 Bytes)	Read/Write
123-126	Reserved (4 Bytes)	Read/Write
127	Page Select Byte	Read/Write

Figure 6.2 Low Page 00H Memory Map

Byte Address	Description	Type
128-175	Module Thresholds (48 Bytes)	Read Only
176-223	Reserved (48 Bytes)	Read Only
224-225	Reserved (2 Bytes)	Read Only
226-239	Reserved (14 Bytes)	Read/Write
240-241	Channel Controls (2 Bytes)	Read/Write
242-253	Reserved (12 Bytes)	Read/Write
254-255	Reserved (2 Bytes)	Read/Write

Figure 6.3 Upper Page 03H Memory Map

This structure permits timely access to addresses in the lower page such as interrupt flags and monitors. Less time critical entries such as serial ID information and threshold settings are available with the page select function. Data used for interrupt handling is located in Lower Page 00h to enable single block read operations for time critical data. Upper Page 01h and Upper Page 02h are optional. Upper Page 01h allows implementation of application select table while Upper Page 02h provides a user read/write space. Implementation of these two pages is optional. Lower and Upper Page 00h are always implemented. Page 03h is required if Page 00h Byte 2 bit 2 is low. Pages 20-7Fh are reserved for future use. Writing the value of a non-supported page shall not be accepted by the transceiver. The Page Select byte shall revert to 0 and read/write operations shall be to Upper Page 00h. Pages 04-1Fh and 80-FFh are for vendor specific functions. Page02 is user EEPROM and its format dedicated by user. The detail description of low memory and Page00 .Page03 upper memory please see SFF-8436 document.

Address	Name	Description
128	Identifier (1 Byte)	Identifier Type of serial transceiver
129	Ext. Identifier (1 Byte)	Extended Identifier of serial transceiver
130	Connector (1 Byte)	Code for connector type
131-138	Transceiver (8 Bytes)	Code for electronic compatibility or optical compatibility
139	Encoding (1 Byte)	Code for serial encoding algorithm
140	BR, nominal (1 Byte)	Nominal bit rate, units of 100 Mbits/s
141	Extended RateSelect Compliance (1 Byte)	Tags for Extended RateSelect compliance
142	Length SMF (1 Byte)	Link length supported for SM fiber in km
143	Length E-50 μm (1 Byte)	Link length supported for EBW 50/125 μm fiber, units of 2 m
144	Length 50 μm (1 Byte)	Link length supported for 50/125 μm fiber, units of 1 m
145	Length 62.5 μm (1 Byte)	Link length supported for 62.5/125 μm fiber, units of 1 m
146	Length copper (1 Byte)	Link length supported for copper, units of 1 m
147	Device Tech (1 Byte)	Device technology
148-163	Vendor name (16 Bytes)	QSFP vendor name (ASCII)
164	Extended Transceiver (1 Byte)	Extended Transceiver Codes for InfiniBand [†]
165-167	Vendor OUI (3 Bytes)	QSFP vendor IEEE vendor company ID
168-183	Vendor PN (16 Bytes)	Part number provided by QSFP vendor (ASCII)
184-185	Vendor rev (2 Bytes)	Revision level for part number provided by vendor (ASCII)
186-187	Wavelength (2 Bytes)	Nominal laser wavelength (Wavelength = value / 20 in nm)
188-189	Wavelength Tolerance (2 Bytes)	Guaranteed range of laser wavelength (+/- value) from Nominal wavelength (Wavelength Tol = value / 200 in nm)
190	Max Case Temp (1 Byte)	Maximum Case Temperature in Degrees C
191	CC_BASE (1 Byte)	Check code for Base ID fields (addresses 128-190)
192-195	Options (4 Bytes)	Rate Select, TX Disable, TX Fault, LOS
196-211	Vendor SN (16 Bytes)	Serial number provided by vendor (ASCII)
212-219	Date code (8 Bytes)	Vendor's manufacturing date code
220	Diagnostic Monitoring Type (1 Byte)	Indicates which type of diagnostic monitoring is implemented
221	Enhanced Options (1 Byte)	Indicates which optional enhanced features are implemented
222	Reserved (1 Byte)	Reserved
223	CC_EXT	Check code for the Extended ID Fields (addresses 192-222)
224-255	Vendor Specific (32 Bytes)	Vendor Specific EEPROM

Figure 6.4 Upper page 00H Memory Map

Digital Diagnostic Monitor Accuracy

The following characteristics are defined over recommended operating conditions.

Parameter	Accuracy	Unit
Internally measured transceiver temperature	+/-3	°C
Internally measured transceiver supply voltage	+/-3	%
Measured Tx bias current	+/-10	%
Measured Tx output power	+/-3	dB
Measured Rx received average optical power	+/-3	dB

Pin Assignment and Description

QSFP+ Transceiver Pad Layout, host PCB QSFP+ Pinout, and PIN Descriptions are as follows:

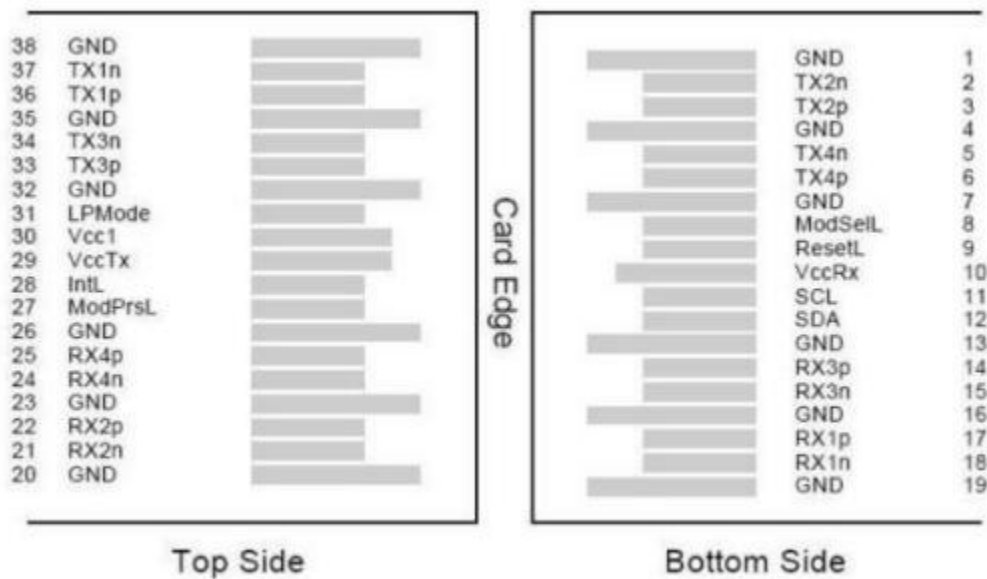


Figure 7.1 QSFP+ Transceiver Electrical Pad Pinout

Pin Description

Pin#	Name	Logic	Description	Power Seq.	Note
1	GND		Ground	1st	1
2	Tx2n	CML-I	Transmitter Inverted Data Input	3rd	
3	Tx2p	CML-I	Transmitter Non-Inverted Data output	3rd	

4	GND		Ground	1st	1
5	Tx4n	CML-I	Transmitter Inverted Data Input	3rd	
6	Tx4p	CML-I	Transmitter Non-Inverted Data output	3rd	
7	GND		Ground	1st	1
8	ModSelL	LVTLL-I	Module Select	3rd	
9	ResetL	LVTLL-I	Module Reset	3rd	
10	VccRx		+3.3V Power Supply Receiver	2nd	2
11	SCL	LVCNOS-I/O	2-Wire Serial Interface Clock	3rd	
12	SDA	LVCNOS-I/O	2-Wire Serial Interface Data	3rd	
13	GND		Ground	1st	1
14	Rx3p	CML-O	Receiver Non-Inverted Data Output	3rd	
15	Rx3n	CML-O	Receiver Inverted Data Output	3rd	
16	GND		Ground	1st	1
17	Rx1p	CML-O	Receiver Non-Inverted Data Output	3rd	
18	Rx1n	CML-O	Receiver Inverted Data Output	3rd	
19	GND		Ground	1st	1
20	GND		Ground	1st	1
21	Rx2n	CML-O	Receiver Inverted Data Output	3rd	
22	Rx2p	CML-O	Receiver Non-Inverted Data Output	3rd	
23	GND		Ground	1st	1
24	Rx4n	CML-O	Receiver Inverted Data Output	3rd	
25	Rx4p	CML-O	Receiver Non-Inverted Data Output	3rd	
26	GND		Ground	1st	1

27	ModPrsL	LVTTL-O	Module Present	3rd	
28	IntL	LVTTL-O	Interrupt	3rd	
29	VccTx		+3.3 V Power Supply transmitter	2nd	2
30	Vcc1		+3.3 V Power Supply	2nd	2
31	LPMode	LVTTL-I	Low Power Mode	3rd	
32	GND		Ground	1st	1
33	Tx3p	CML-I	Transmitter Non-Inverted Data Input	3rd	
34	Tx3n	CML-I	Transmitter Inverted Data Output	3rd	
35	GND		Ground	1st	1
36	Tx1p	CML-I	Transmitter Non-Inverted Data Input	3rd	
37	Tx1n	CML-I	Transmitter Inverted Data Output	3rd	
38	GND		Ground	1st	1

Notes:

1. GND is the symbol for signal and supply (power) common for the QSFP+ module. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.

2. Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power supplies and shall be applied concurrently. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP+ transceiver module in any combination. The connector pins are each rated for a maximum current of 500 mA.

Mechanical Dimensions

Figure 8.1 shows the package dimensions of the module. The module is designed to be compliant with QSFP+ MSA specification. Package dimensions are specified in SFF-8436.

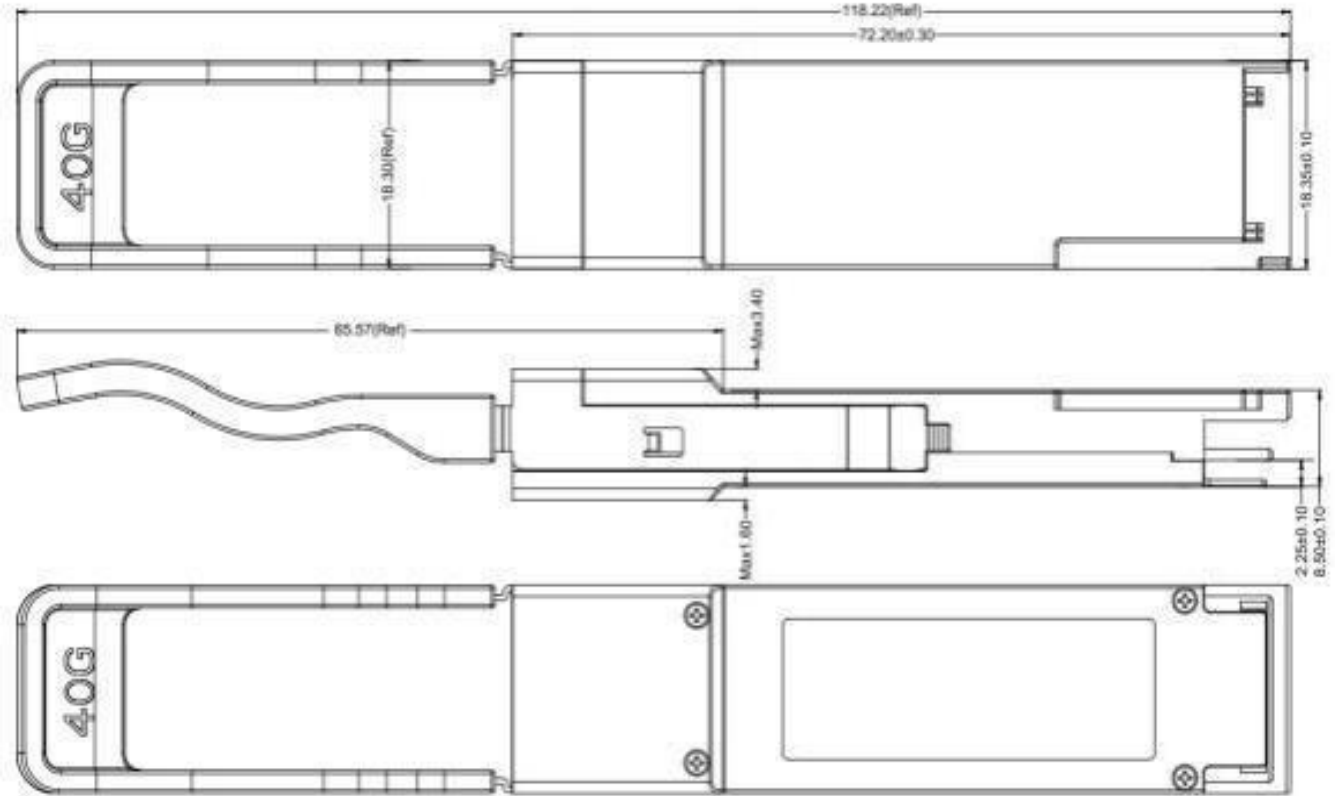
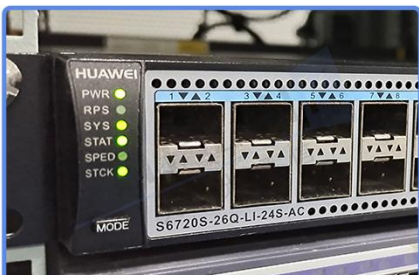


Figure 8.1 Package dimensions

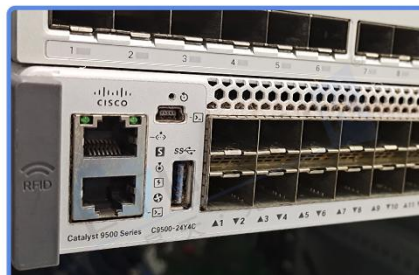
Compatibility Test

In order to ensure the product compatibility, our products will be tested on the switch before shipment. Our modules can compatible with many mainstream brand switches, such as Cisco, Juniper, Extreme, Brocade, IBM, H3C, HP, Huawei, D-Link, Mikrotik, ZTE, TP-Link...

Our test equipment: VOLKTEK MEN-4110, HP 2530-8G, CRS226-24G-25+RM, Catalyst 2960G Series, Catalyst 3850 XS 10G SFP+, Catalyst 3750-E Series, HUAWEI S5700Series, H3C S3100V2 Series, Juniper-EX4200, etc.



HUAWEI S6720S



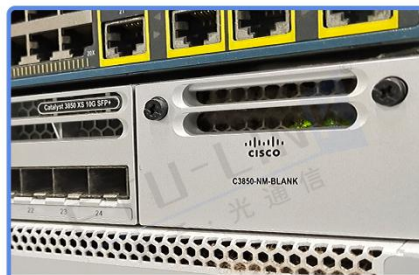
CISCO Catalyst 9500



DELL S5048F



H3C S3100V2



CISCO C3850



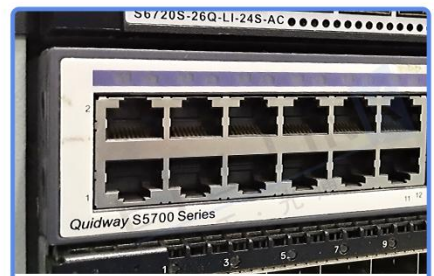
Aruba 2930F



Juniper EX 4200



HP J9264A

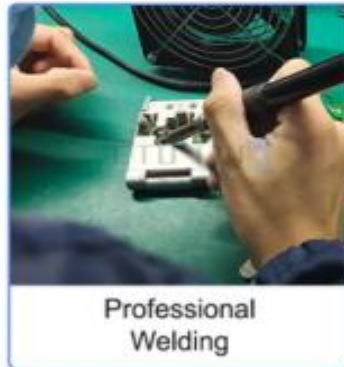


Quidway S5700

Product Production Process

Quality Assurance

Continuous introduction of new equipment,
produced by strict standards, strict quality inspection,
to guarantee the high quality, standard of each product.



Packaging

ETU-Link provides two kinds of packaging, 10pcs/Tray and individual package.



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