Rev	Date	Modified by	Description
A0	2023		

Product Specifications

10.3Gbps SFP+ Transceiver with CDR, Single Mode, 40km Reach

PN: ES31X-3LCD40

Features

➢ Supports up to 11.3Gbps bit rates
➢ **Built-in TX and RX CDR**
➢ Hot-pluggable SFP+ footprint
➢ 1310nm DFB laser and PIN photodiode, Up to 40km for SMF transmission
➢ Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
➢ Compatible with RoHS
➢ Single +3.3V power supply
➢ Real Time Digital Diagnostic Monitoring
➢ Operating case temperature:
   Standard: 0 to +70°C

Applications

➢ 10Gbps Optical systems
➢ SONET OC-192&SDH STM-64 at 9.953Gbps
➢ 10GBASE-ER at 10.3125Gbps
➢ 10GBASE-EW at 9.953Gbps
➢ LTE systems
➢ Other Optical links
➢ OTU2 at 10.7Gbps

Description

The SFP+ transceivers are high performance (**with TX&RX CDR**), cost effective modules supporting data rate of 10Gbps and 40km transmission distance with SMF.

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I

laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.
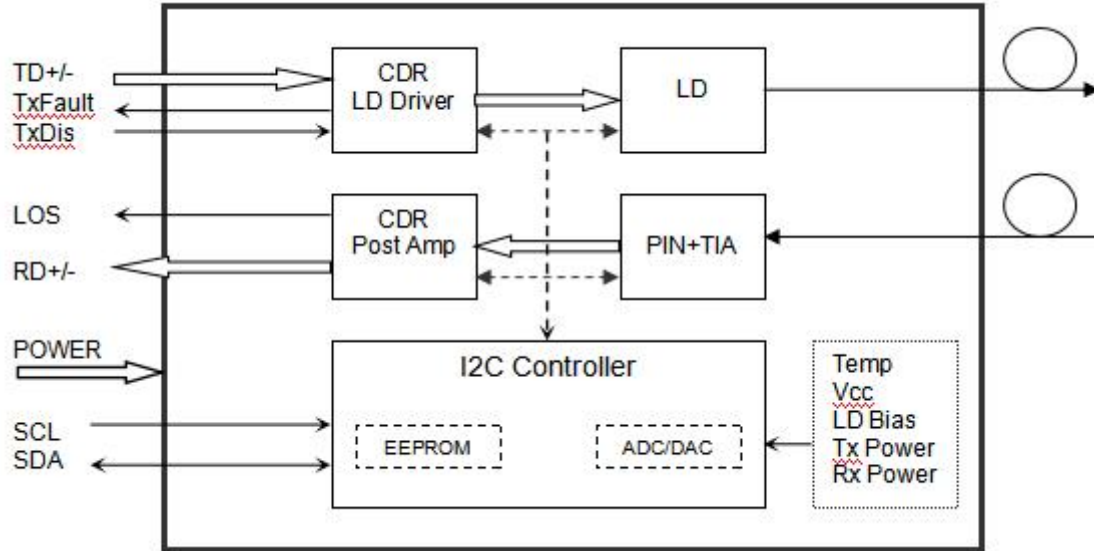
Transceiver functional diagram

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		+70	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			600	mA
Data Rate		9.953	10.3125	11.3	Gbps

Optical and Electrical Characteristics

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Centre Wavelength		λc	1270	1310	1350	nm	
Spectral Width（-20dB）		$\Delta\lambda$			1	nm	
Side-Mode Suppression Ratio		SMSR	30	-		dB	
Average Output Power		P_{out}	-1		+4	dBm	1
Extinction Ratio		ER	3.5			dB	
Data Input Swing Differential		V_{IN}	180		850	mV	2
Input Differential Impedance		Z_{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
	Enable		0		0.8	V	
TX Fault	Fault		2.0		Vcc	V	
	Normal		0		0.8	V	
Receiver							
Centre Wavelength		λc	1260		1600	nm	
Receiver Sensitivity					-16	dBm	3
Receiver Overload			0.5			dBm	3
LOS De-Assert		LOS_D			-17	dBm	
LOS Assert		LOS_A	-30			dBm	
LOS Hysteresis			0.5			dB	
Data Output Swing Differential		V_{out}	300		900	mV	4
LOS	High		2.0		Vcc	V	
	Low				0.8	V	

Notes:

1. The optical power is launched into SMF.

2. PECL input, internally AC-coupled and terminated.

3. Measured with a PRBS $2^{31}-1$ test pattern @10312Mbps, BER $\leq 1\times10^{-12}$.

4. Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	µs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	µs
Tx Disable To Reset	t_reset	10			µs
LOS Assert Time	t_loss_on			100	µs
LOS De-assert Time	t_loss_off			100	µs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V_H	2		Vcc	V
MOD_DEF (0:2)-Low	V_L			0.8	V

Diagnostics

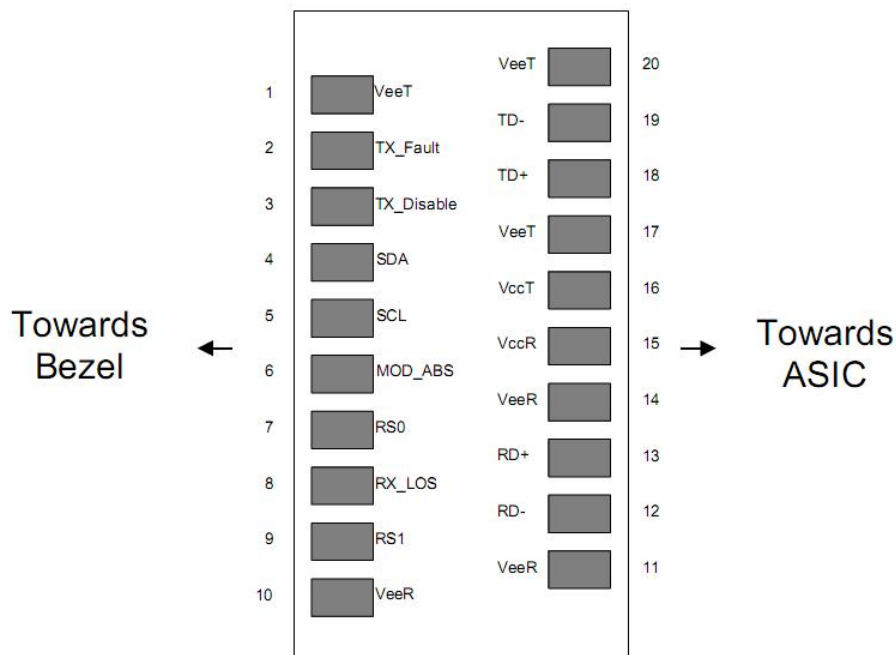| Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-6.5 to -0.5	dBm	±3dB	Internal
RX Power	-20 to -1	dBm	±3dB	Internal

Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.

**2 wire address 1010000X (A0h)**

Offset	Content
0	
	Serial ID Defined by SFP MSA (96 bytes)
95	
	Vendor Specific (32 bytes)
127	
	Reserved in SFP MSA (128 bytes)
255	

2 wire address 1010001X (A2h)

Offset	Content
0	
	Alarm and Warning Thresholds (56 bytes)
55	
	Cal Constants (40 bytes)
95	
	Real Time Diagnostic Interface (24 bytes)
119	
	Vendor Specific (8 bytes)
127	
	User Writable EEPROM (120 bytes)
247	
	Vendor Specific (8 bytes)
255	

Pin Descriptions

Towards Bezel ←

Pin	Name
1	VeeT
2	TX_Fault
3	TX_Disable
4	SDA
5	SCL
6	MOD_ABS
7	RS0
8	RX_LOS
9	RS1
10	VeeR

→ Towards ASIC

Name	Pin
VeeT	20
TD-	19
TD+	18
VeeT	17
VccT	16
VccR	15
VeeR	14
RD+	13
RD-	12
VeeR	11

Pin	Signal Name	Description	Plug Seq.	Notes
1	V_{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V_{EER}	Receiver ground	1	
11	V_{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V_{EER}	Receiver ground	1	
15	V_{CCR}	Receiver Power Supply	2	
16	V_{CCT}	Transmitter Power Supply	2	
17	V_{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V_{EET}	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.

2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.

3) LOS is open collector output.   Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.

5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

# Recommended Interface Circuit
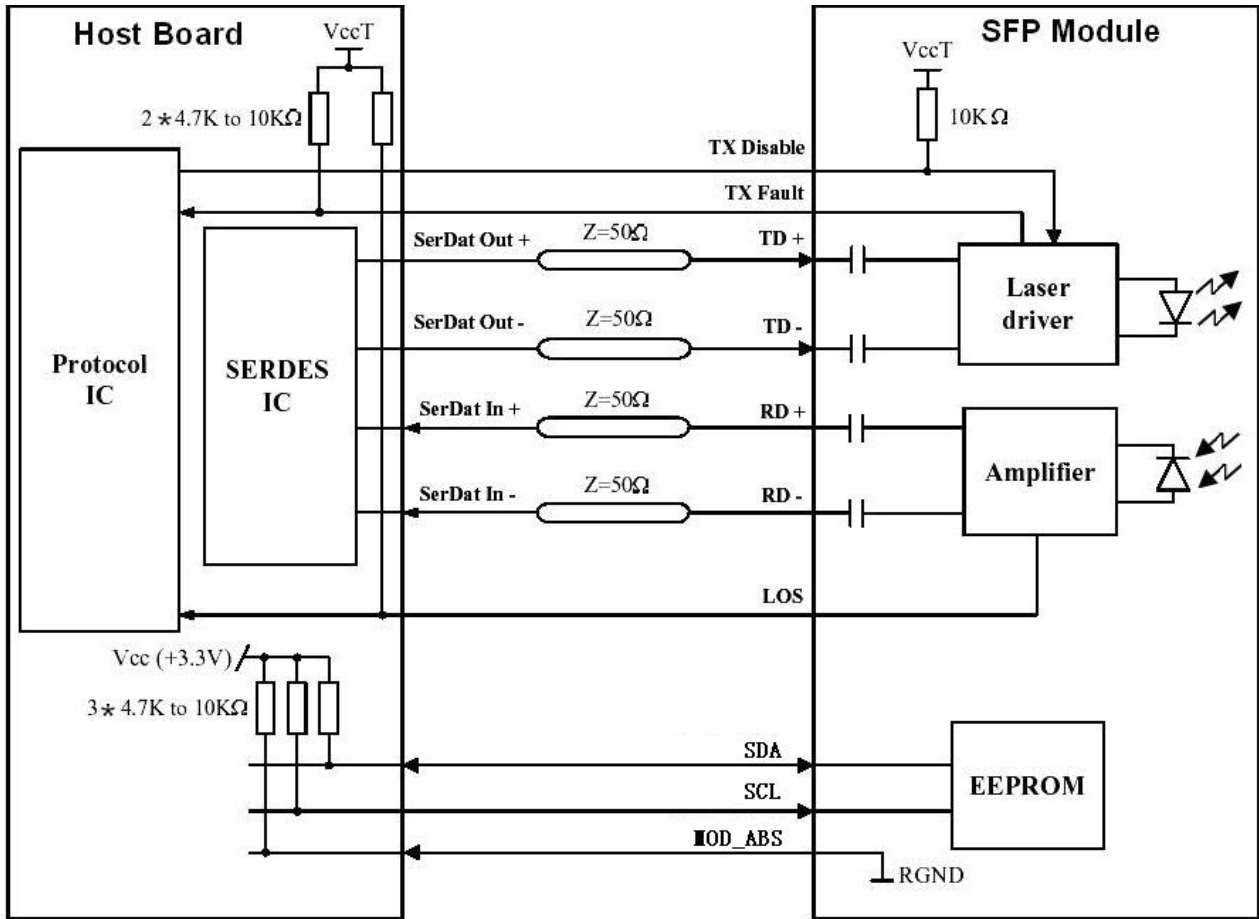
Mechanical Dimensions
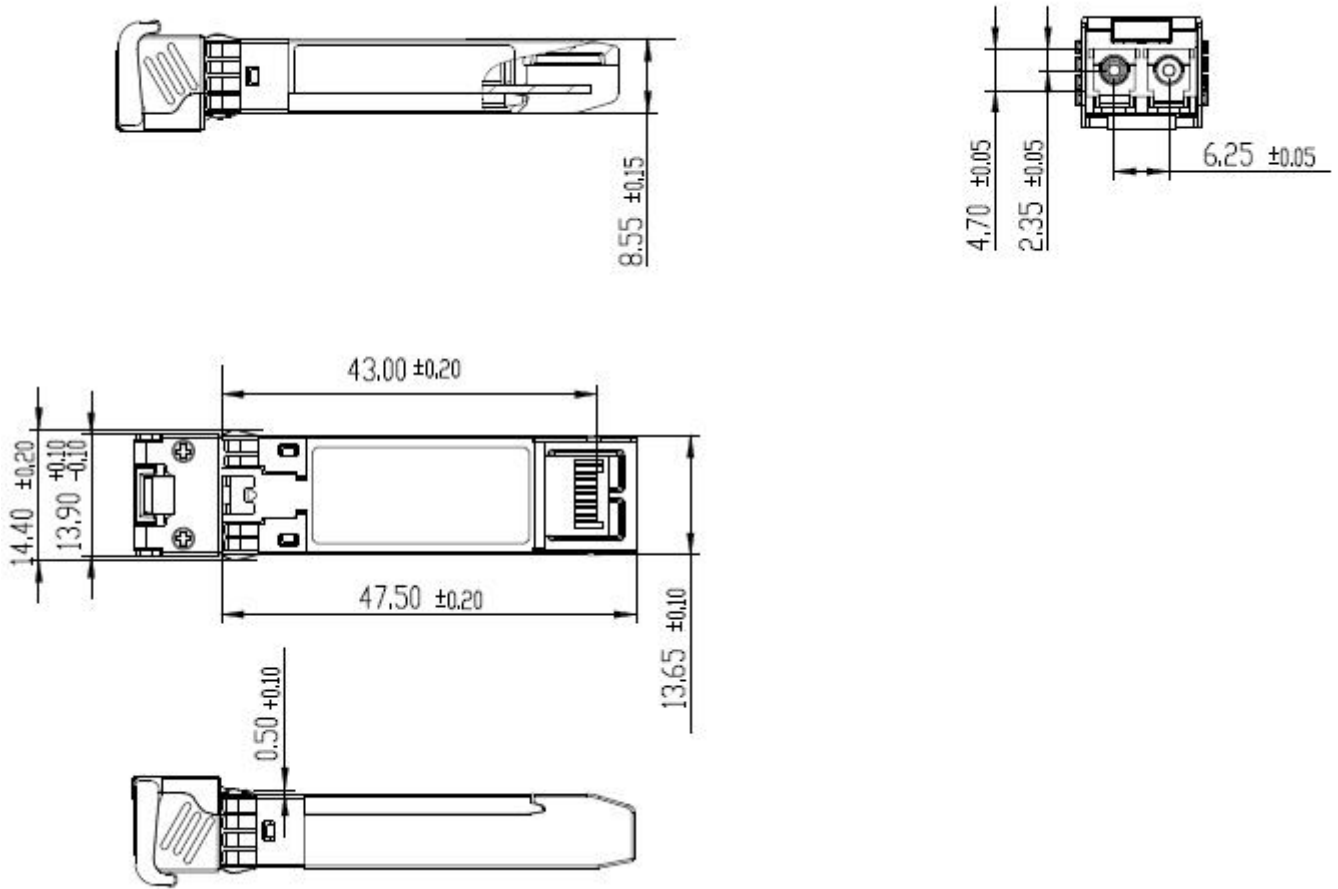
Ordering information

| Part Number	Description
ES31X-3LCD40	1310nm, 10.3Gbps with Tx and Rx CDR, LC, 40km, 0°C~+70°C，with DDM

Compatibility Test

In order to ensure the product compatibility, our products will be tested on the switch before shipment. Our modules can compatible with many mainstream brand switches, such as Cisco, Juniper, Extreme, Brocade, IBM, H3C, HP, Huawei, D-Link, Mikrotik, ZTE, TP-Link...

Our test equipment: VOLKTEK MEN-4110, HP 2530-8G, CRS226-24G-25+RM, Catalyst 2960G Series, Catalyst 3850 XS 10G SFP+, Catalyst 3750-E Series, HUAWEI S5700Series, H3C S3100V2 Series, Juniper-EX4200, etc.
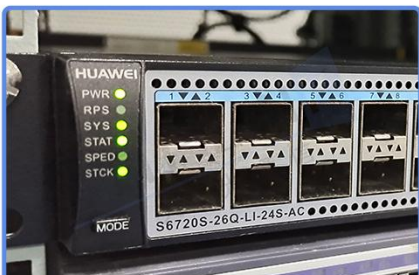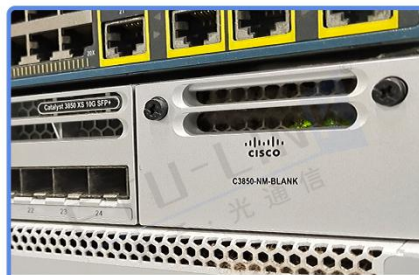

HUAWEI S6720S
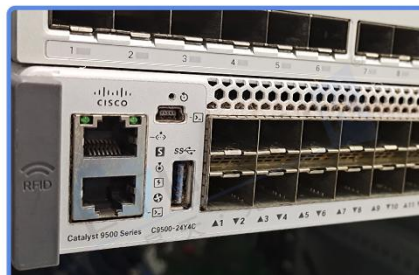
CISCO Catalyst 9500
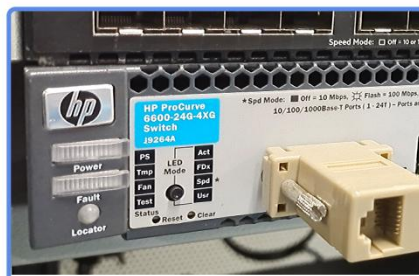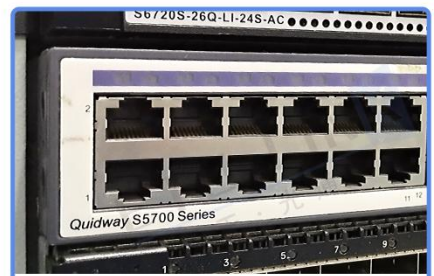

DELL S5048F

H3C S3100V2

CISCO C3850

Aruba 2930F
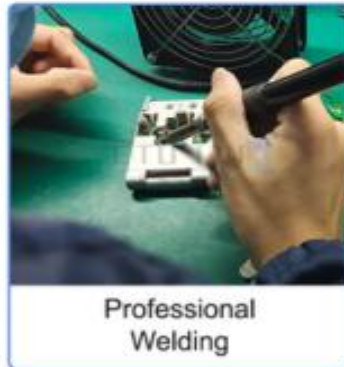

Juniper EX 4200

HP J9264A

Quidway S5700

Product Production Process

Quality Assurance

Continuous introduction of new equipment, produced by strictstandards,strict quality inspection, to guarantee the high quality,standard of each product.

Standardized Production Line → Professional Welding → Assembling

Aging Testing ← Distance Testing ← Cleaning End Face

Product Initial Test → Switch Testing → Product Final Test

Packaging

ETU-Link provides two kinds of packaging, 10pcs/Tray and individual package.

Inner Tray

Anti-static bag

White

10pcs/ Tray

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Addresses and phone number also have been listed at www.etulinktechnology.com.
Please e-mail us at sales@etulinktechnology.com or call us for assistance.