

ESP5542-80D(I)

4.25Gbps 1550nm 80KM SFP Transceiver

PRODUCT FEATURES

- **Data-rate Supports 1.0625/2.125/4.25Gb/s Fiber Channel Operation**
- **1550nm DFB laser and APD photo detector for 80KM transmission**
- **SFP MSA SFF-8074i compliant**
- **Digital Diagnostic SFF-8472 compliant**
- **Digital Diagnostic Monitoring:**
Internal Calibration or External Calibration
- **Compatible with RoHS**
- **Operating case temperature:**
Standard: 0 to +70°C
Industrial: -40 to +85°

APPLICATIONS

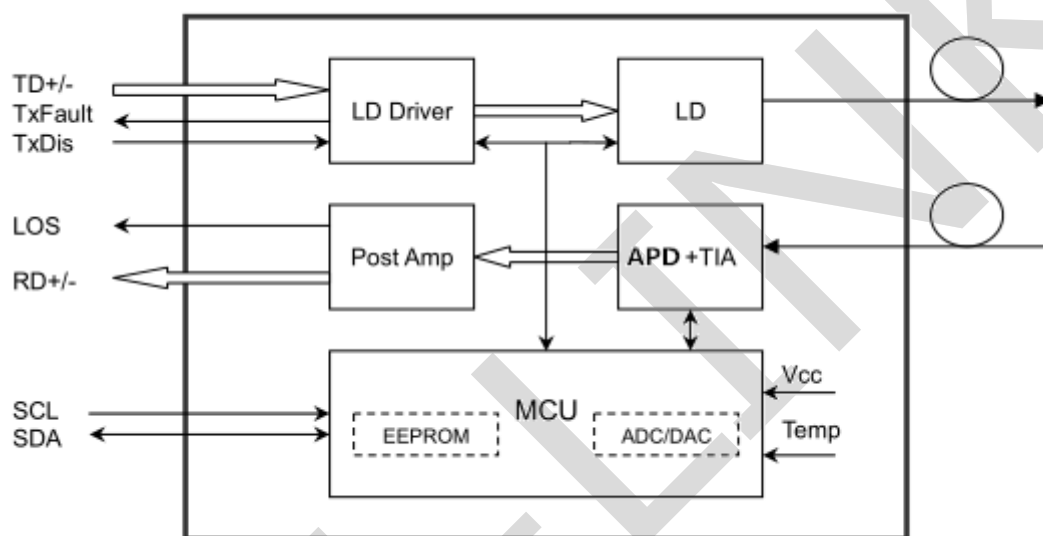
- **Tri Rate 1.0625 / 2.125 / 4.25Gbp/s Fiber Channel**
- **1.25Gbp/s 1000Base-LX Ethernet and 1000Base-LX10 (Rate selectable version)**

DESCRIPTIONS

The transceiver consists of three sections: a DFB laser transmitter, an APD photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with the Small Form Factor Pluggable Multi-Sourcing Agreement (MSA)1. They are compatible with Fiber Channel per FC-P1-2 Rev. 10.0. Also simultaneously compatible with Gigabit Ethernet as specified in IEEE STD 802.3.

Module Block Diagram



Ordering Information

Part No.	Data Rate(optical)	Laser	Fiber Type	Distance	Optical Interface	Temp	DDMI
ESP5542-80D	4.25Gbps	DFB	SMF	80KM	LC	0~70℃	Y
ESP5542-80DI	4.25Gbps	DFB	SMF	80KM	LC	-40~85℃	Y

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Maximum Supply Voltage	Vcc	-0.5		4.7	V	
Storage Temperature	TS	-40		85	℃	
Case Operating Temperature(Commercial)	To	0		70	℃	
Case Operating Temperature (Industrial)	To	-40		85	℃	

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Case Temperature	Tcase	0		70	°C	
		-40		85	°C	
Ambient Humidity	HA	5		85	%	
Power Supply Voltage	VCC	3.14	3.3	3.46	V	
Power Supply Current	ICC			300	mA	
Power Supply Noise Rejection				100	mVp-p	100Hz to 1MHz
Transmission Distance				80	km	
Coupled fiber	Single mode fiber					ITU-T G.653

Electrical Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Supply Voltage	Vcc	3.15	3.3	3.6	V	
Supply Current	Icc		250	300	mA	
Transmitter (Module Input)						
Input differential impedance	Rin		100		Ω	1
Single ended data input swing	Vin,pp	250		1200	mV	
Transmit Disable Voltage	VD	Vcc-1.3		Vcc	V	
Transmit Enable Voltage	VEN	Vee		Vee+ 0.8	V	2
Transmit Disable Assert Time				10	us	
Receiver (Module Output)						
Single ended data output swing	Vout,pp	250		800	mV	3
Data output rise time	tr		100	175	ps	4
Data output fall time	tf		100	175	ps	4
LOS Fault	VLOS fault	Vcc-0.5		VccHOST	V	5
LOS Normal	VLOS norm	Vee		Vee+0.5	V	5
Power Supply Rejection	PSR	100			mVpp	6

Notes:

1. Connected directly to TX data input pins. AC coupled thereafter.
2. Or open circuit.
3. into 100 ohms differential termination.
4. 20 – 80 %
5. Loss Of Signal is LVTTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.

Optical and Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Transmitter						
Output Opt. Pwr (End of Life)	POUT	2.0		5.0	dBm	1
Optical Wavelength	λ	1510	1550	1590	nm	
Wavelength Temperature Dependence			0.08	0.125	nm/°C	
Spectral Width (-20dB)	σ			3.0	nm	
Optical Extinction Ratio	ER	9			dB	
Sidemode Suppression ratio	SSRmin	30			dB	
Optical Rise/Fall Time	tr/ tf		100	160	ps	
RIN	RIN			-120	dB/Hz	
Transmitter Jitter (peak to peak)				100	ps	
Receiver						
Average Rx Sensitivity @ Gigabit Ethernet	RSSENS3			-28.0	dBm	2
Maximum Input Power	PMAX	-9.0			dBm	
Optical Center Wavelength	λ_C	1260		1620	nm	
LOS De -Assert	LOSD			-30	dBm	
LOS Assert	LOSA	-40			dBm	
LOS Hysteresis			1.0		dB	
Receiver Jitter Generation @4.25Gbps				160	ps	3

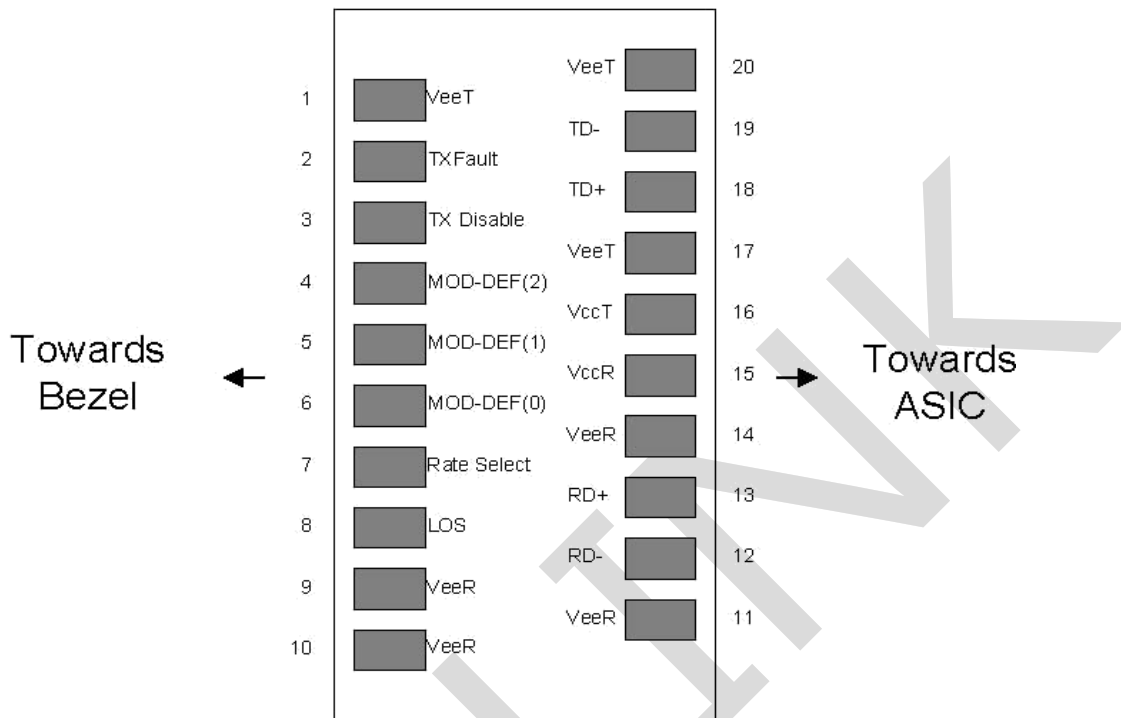
Notes:

1. The optical power is launched into SMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 27-1 test pattern @4250Mbps, BER $\leq 1 \times 10^{-12}$.

Digital Diagnostics

Parameter	Range	Accuracy	Unit	Calibration
Temperature	-40 to 85	± 3	°C	Internal
Voltage	0 to 3.6	$\pm 3\%$	V	Internal
Tx Bias Current	0 to 100	$\pm 10\%$	mA	Internal
Tx Output Power	2 to 5	± 3	dB	Internal
Rx Input Power	-28 to -9	± 3	dB	Internal

Pin Diagram



Pin Definitions

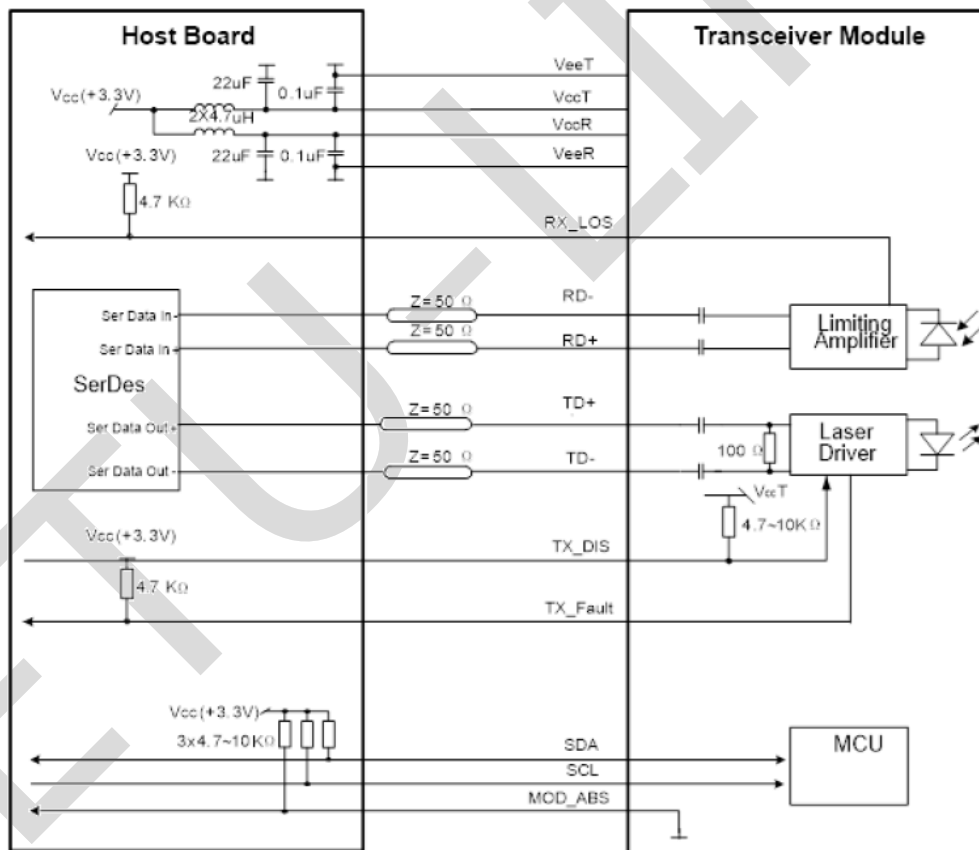
PIN #	Name	Function	Notes
1	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1
2	T_{FAULT}	Transmitter Fault.	2
3	T_{DIS}	Transmitter Disable. Laser output disabled on high or open.	3
4	MOD_DEF(2)	Module Definition 2. Data line for Serial ID.	4
5	MOD_DEF(1)	Module Definition 1. Clock line for Serial ID.	4
6	MOD_DEF(0)	Module Definition 0. Grounded within the module.	4
7	Rate Select	No connection required	
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
10	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
11	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
15	V_{CCR}	Receiver Power Supply	
16	V_{CCT}	Transmitter Power Supply	
17	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1

18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1

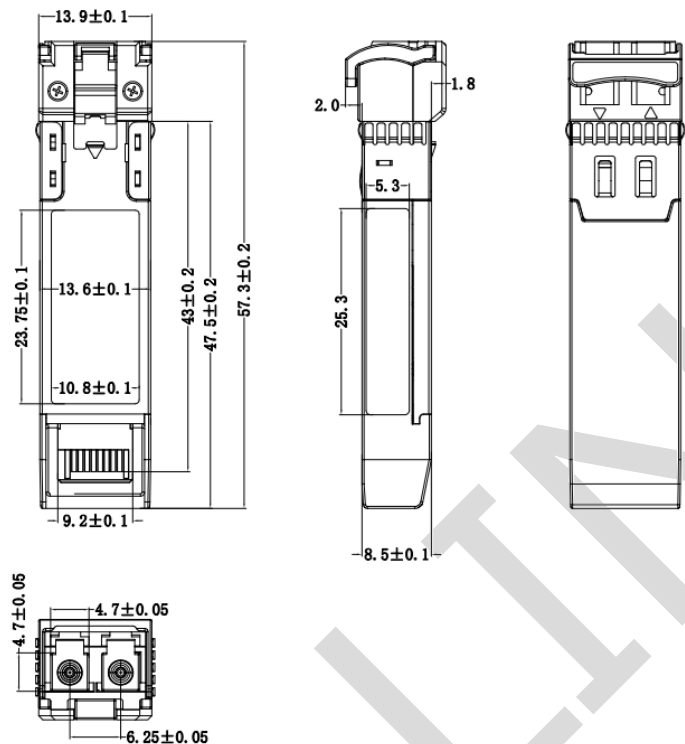
Notes:

1. Circuit ground is internally isolated from chassis ground.
2. T_{FAULT} is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to V_{cc} + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
3. Laser output disabled on T_{DIS} >2.0V or open, enabled on T_{DIS} <0.8V.
4. Should be pulled up with 4.7k - 10kohms on host board to a voltage between 2.0V and 3.6V. MOD_DEF (0) pulls line low to indicate module is plugged in.
5. LOS is open collector output. Should be pulled up with 4.7k – 10 kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

Recommended Interface Circuit



Mechanical Diagram



Revision History

Version No.	Date	Description
1.0	May 18, 2016	Preliminary datasheet
2.0	September 28, 2018	Product upgrades
3.0	Aug 25, 2024	Format change

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